

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a plan view, in enlarged scale, of a transponder configuration.

FIG. 2 is a plan view, in greatly enlarged scale, of a portion of the transponder configuration shown in FIG. 1.

FIG. 3 is a diagram showing the respective time delays of the different SAW pathways in the transponder of FIG. 1.

FIG. 4 is a flow diagram showing the order of calculations carried out by the signal processor.

FIG. 5 is a block diagram of a first embodiment of an acoustic transponder interrogation system according to the present invention.

FIG. 6 is a block diagram of a second embodiment of an acoustic transponder interrogation system according to the present invention, having a plurality of signal generators.

FIGS. 7A1, 7A2 and 7B are schematic drawings of a single pole R-C integrator, a double pole R-C integrator, and a switched integrator.

FIG. 8 is a flow chart showing the operation sequence of a system according to the present invention.

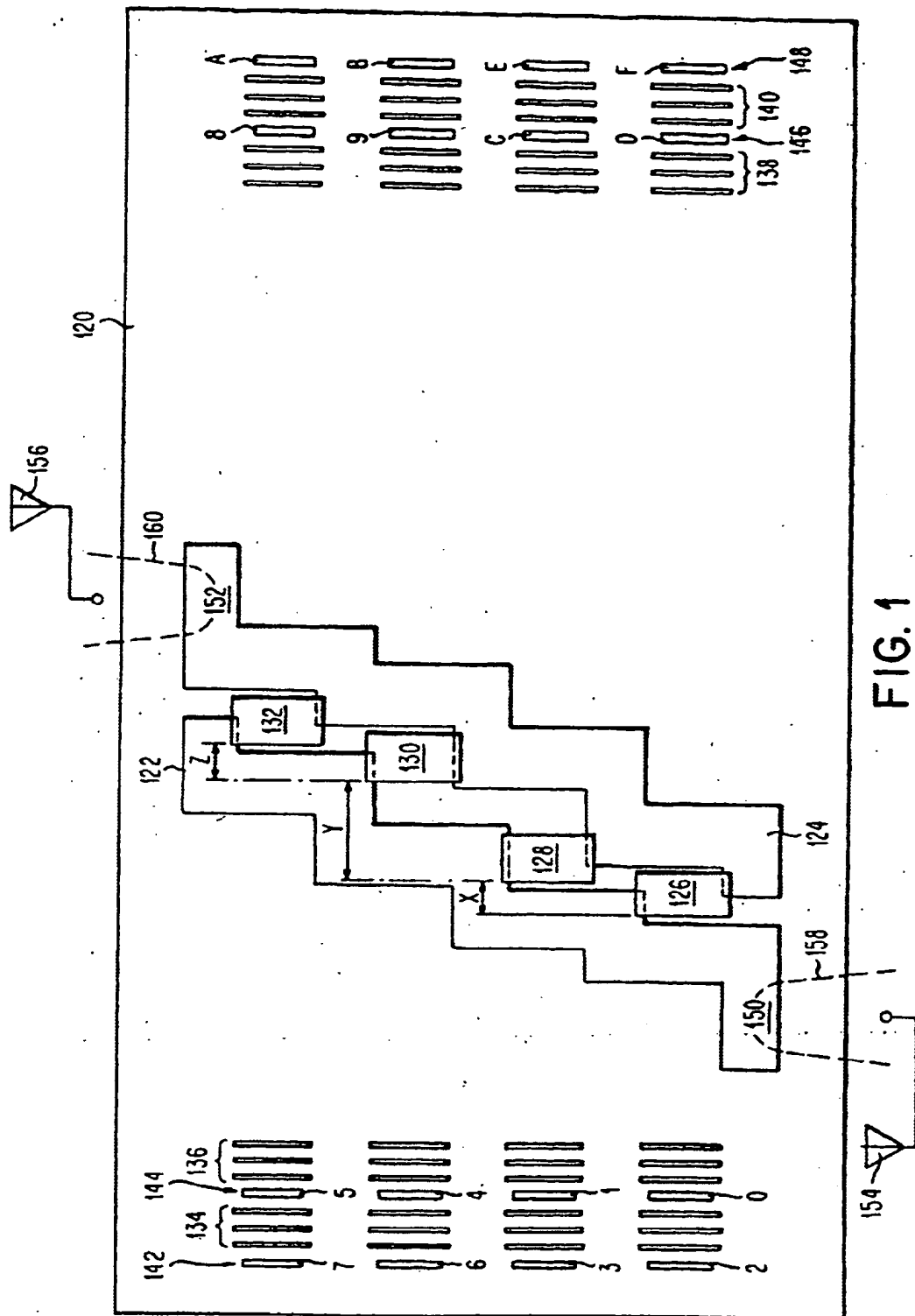


FIG. 1

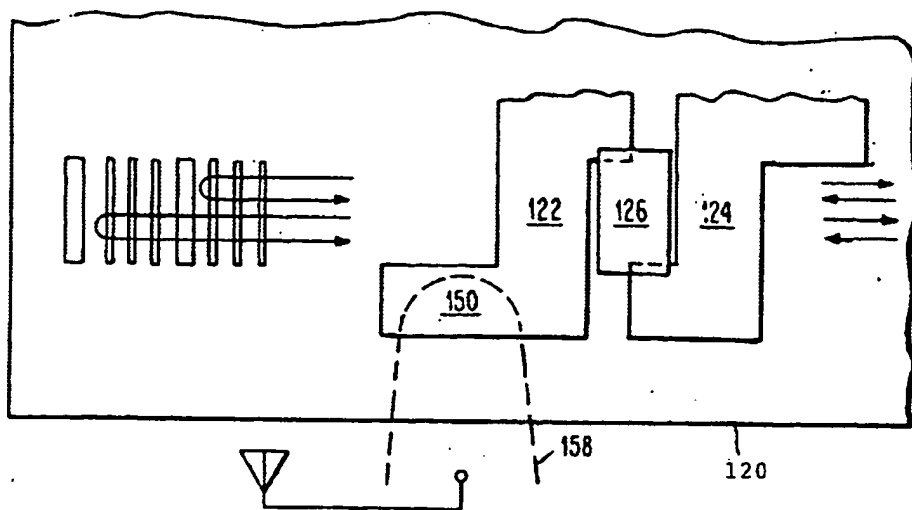


FIG. 2

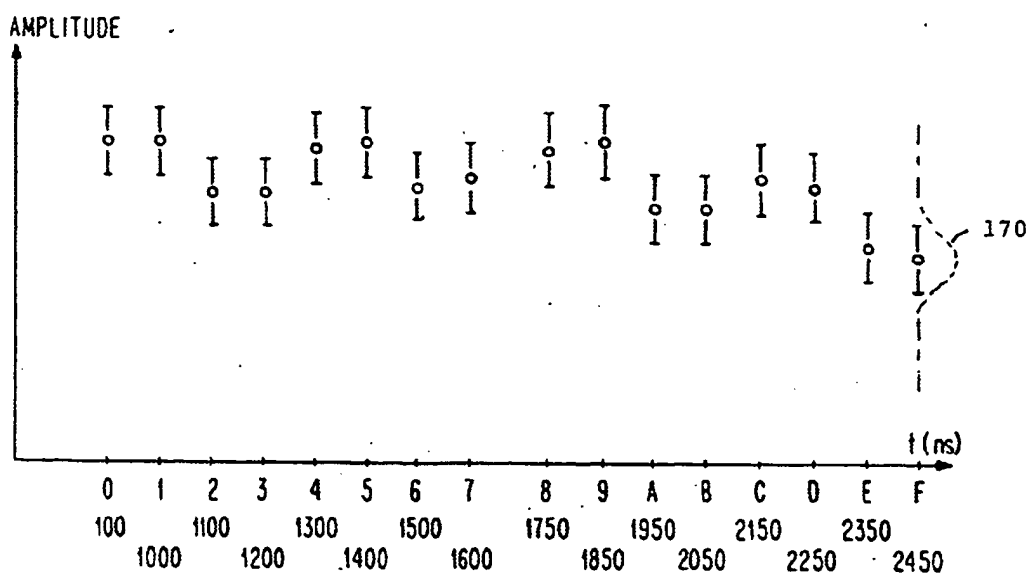


FIG. 3

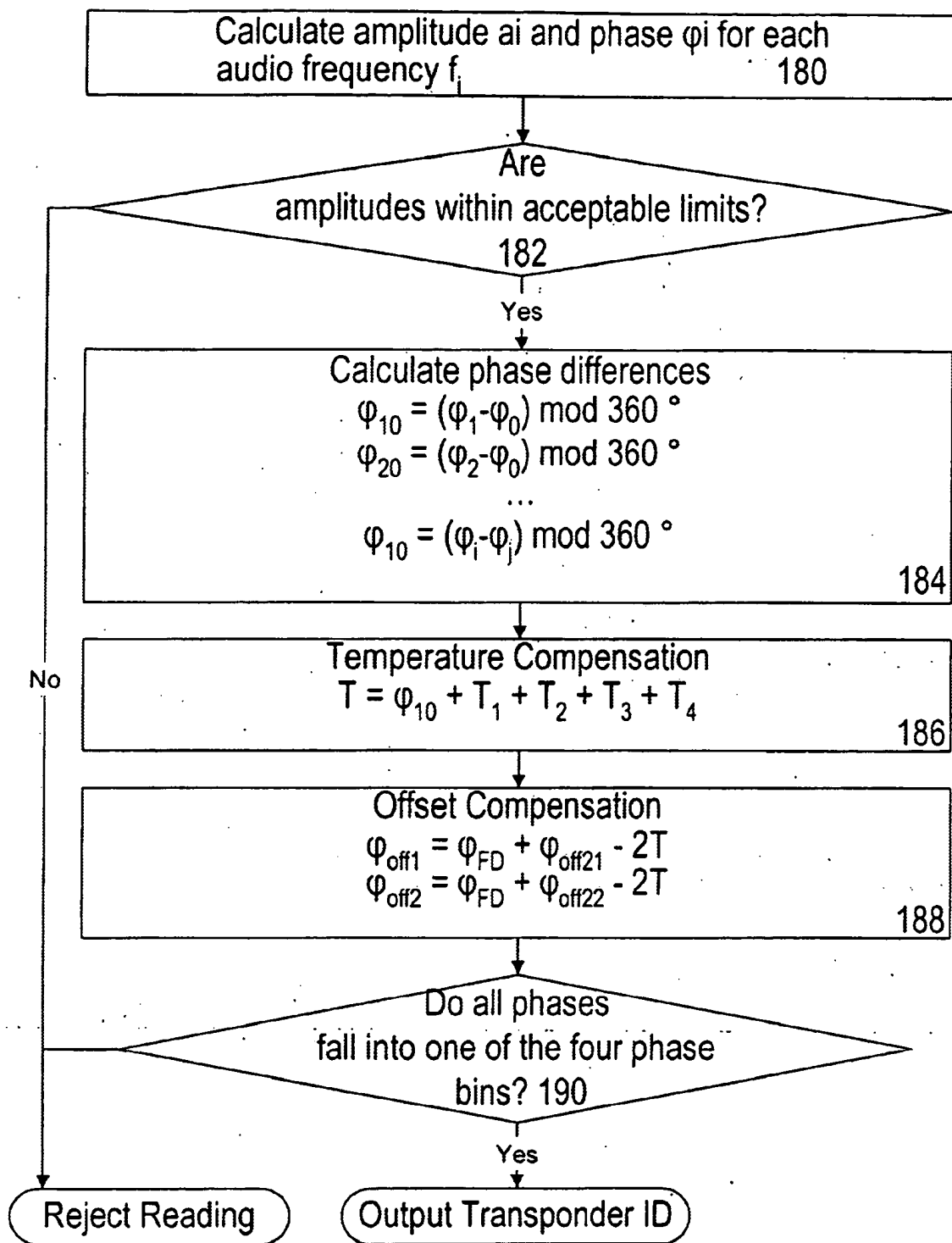


FIG. 4

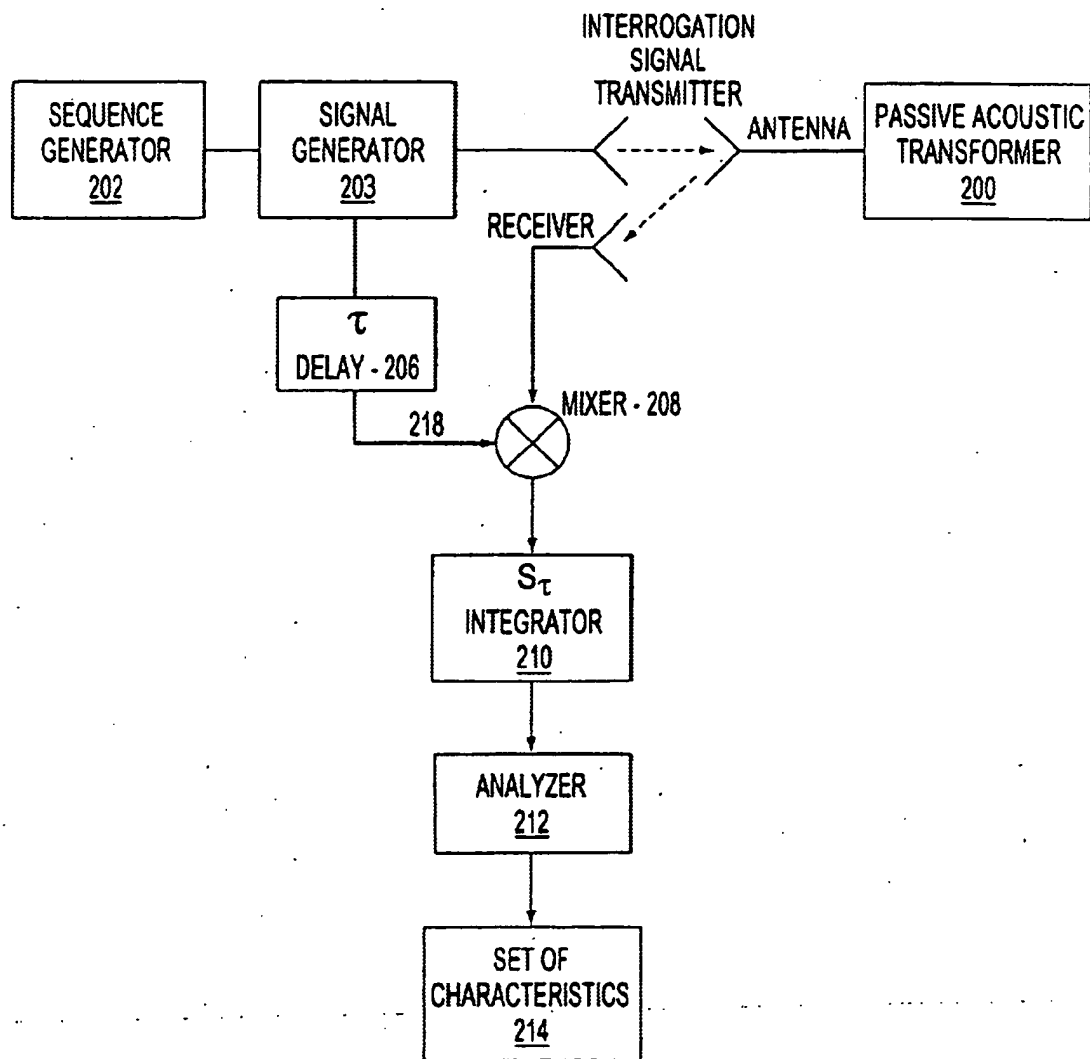


FIG. 5

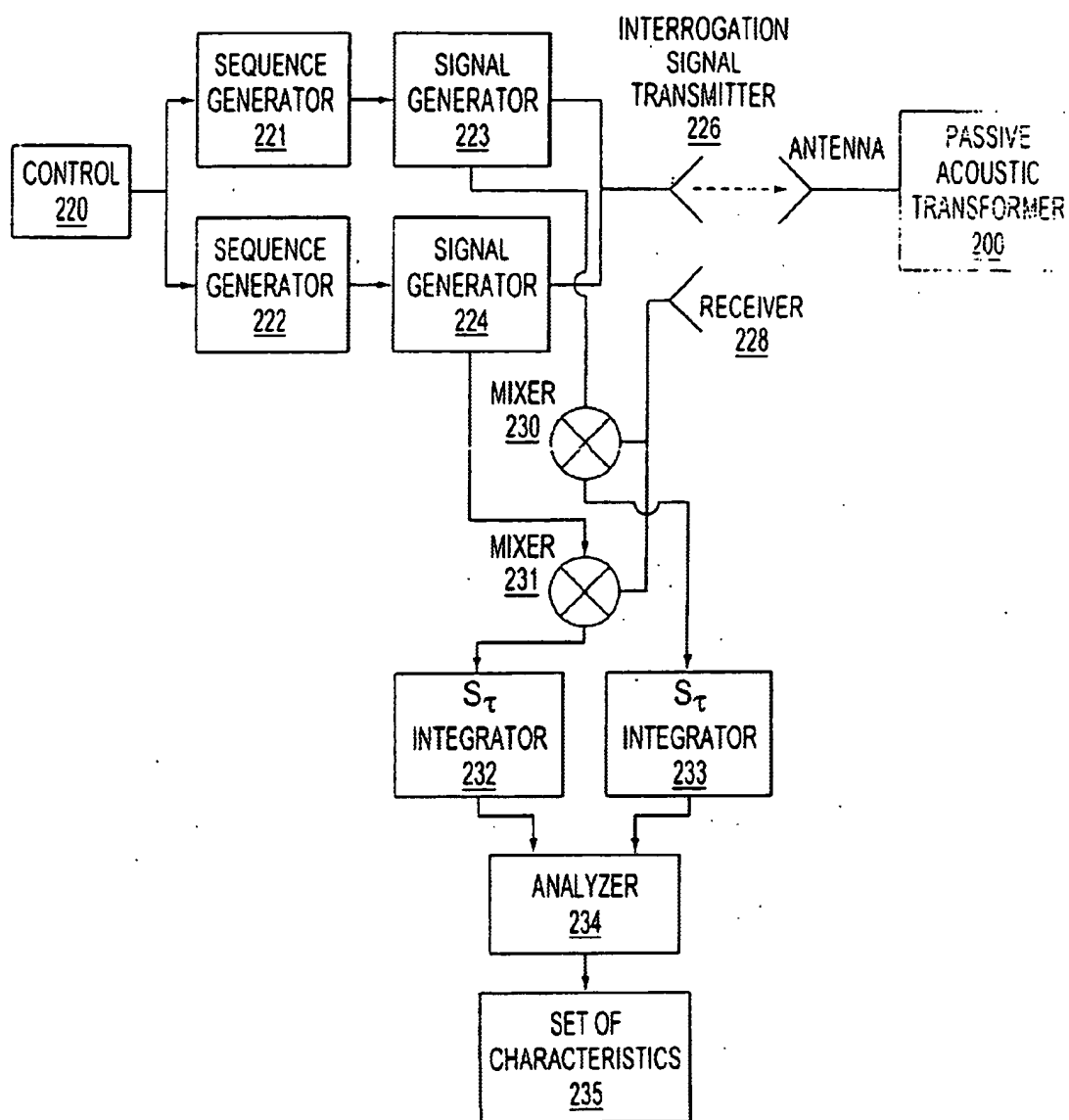


FIG. 6

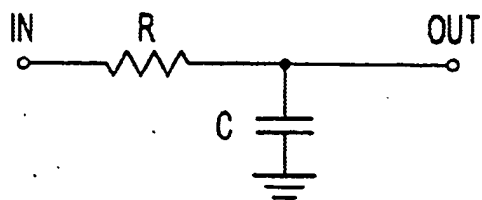


FIG. 7A1

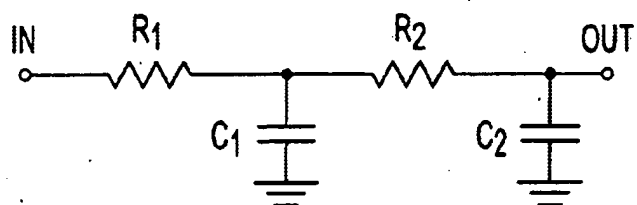


FIG. 7A2

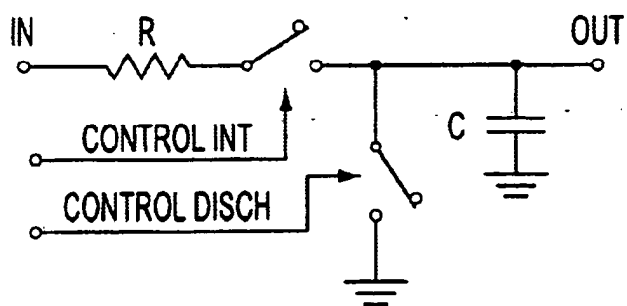


FIG. 7B

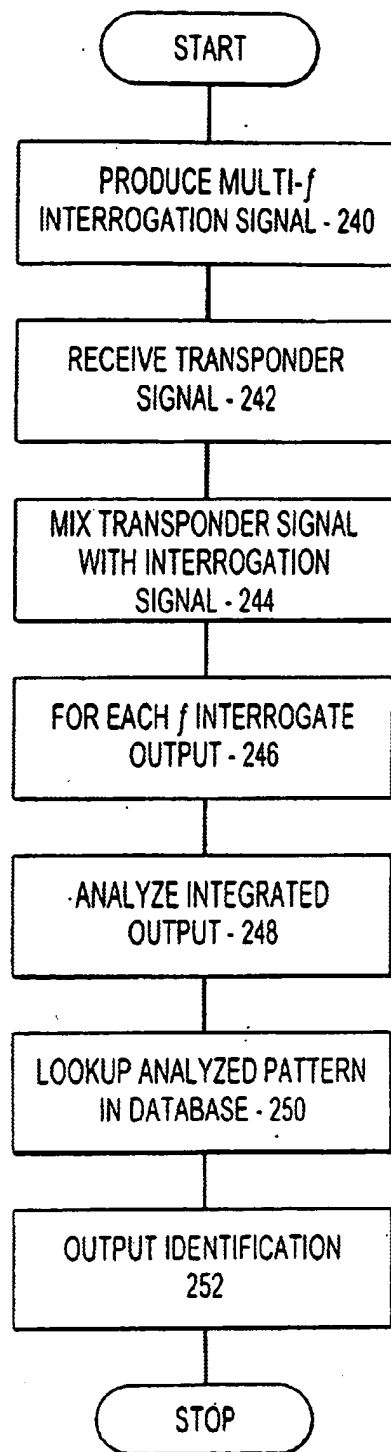


FIG. 8